

FIGURE 1

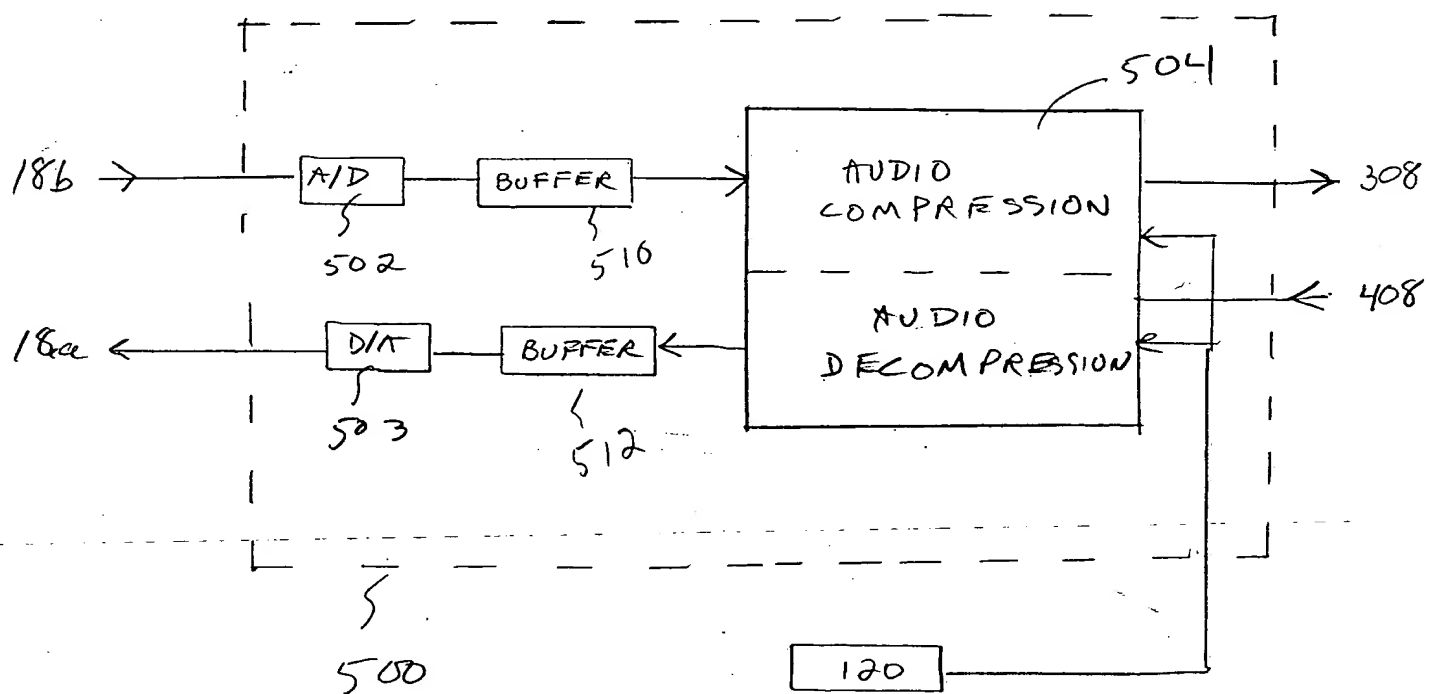


FIGURE 3C

FIG. 2

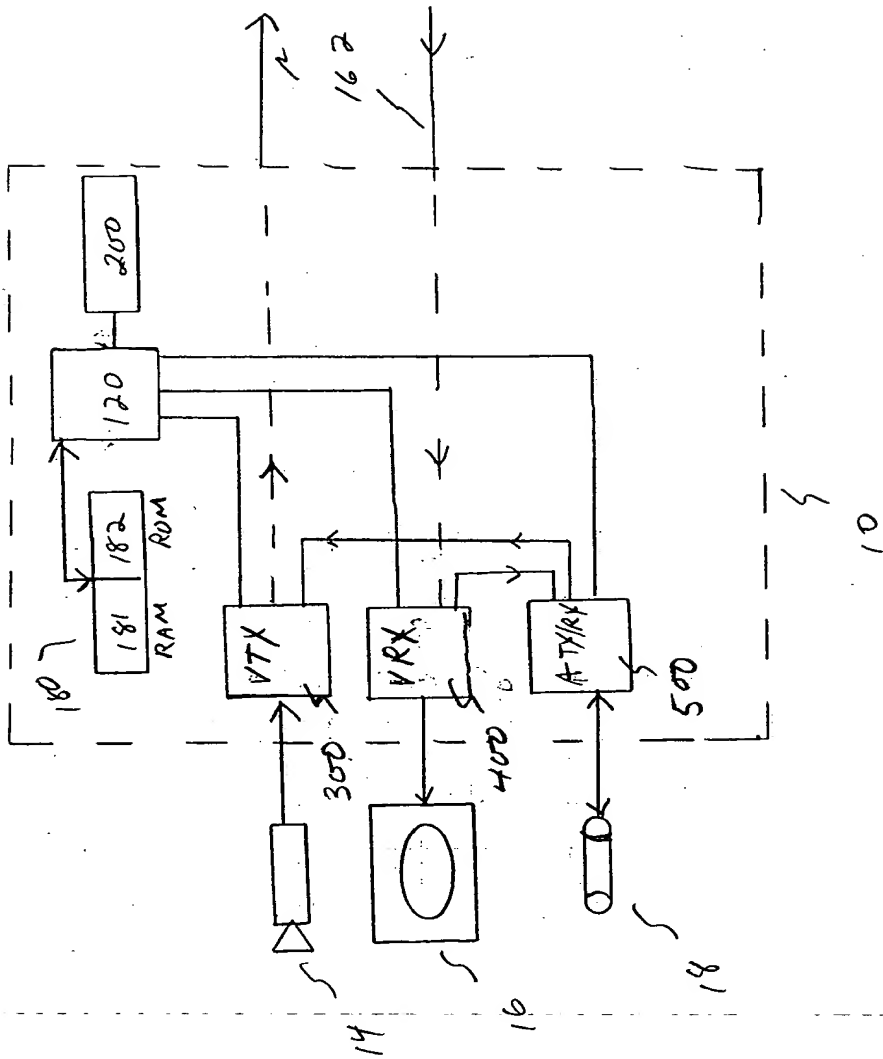


FIGURE 2

304 Video Codec TX Board

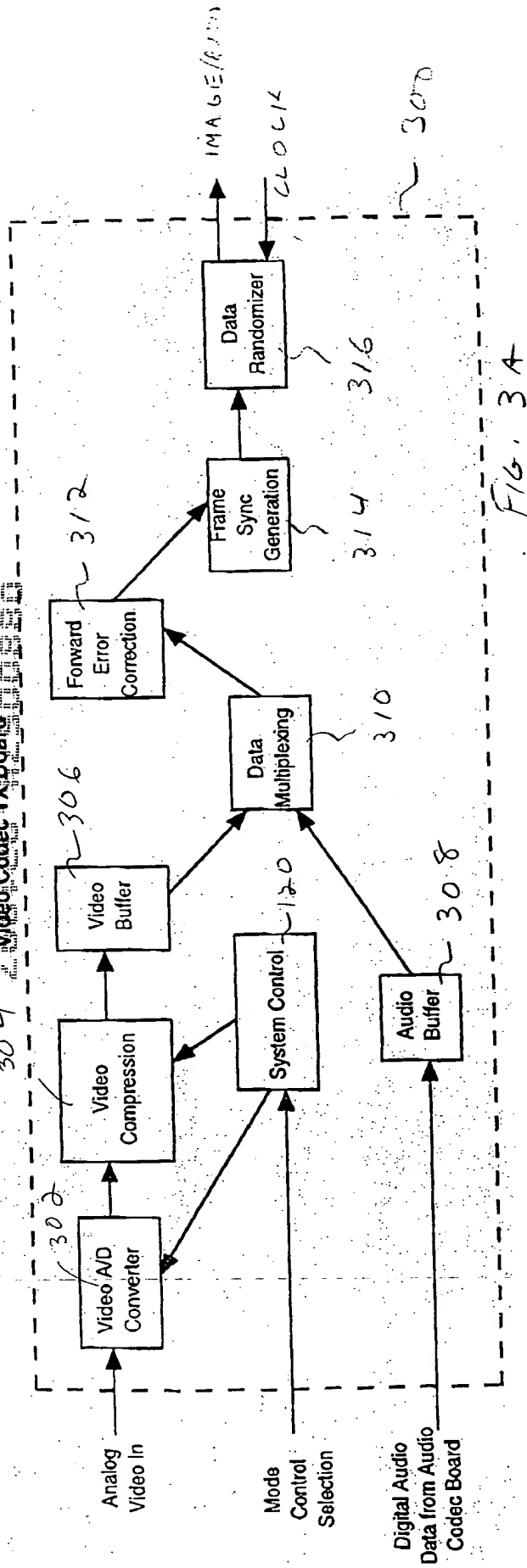


FIG. 3A

Video Codec RX Board

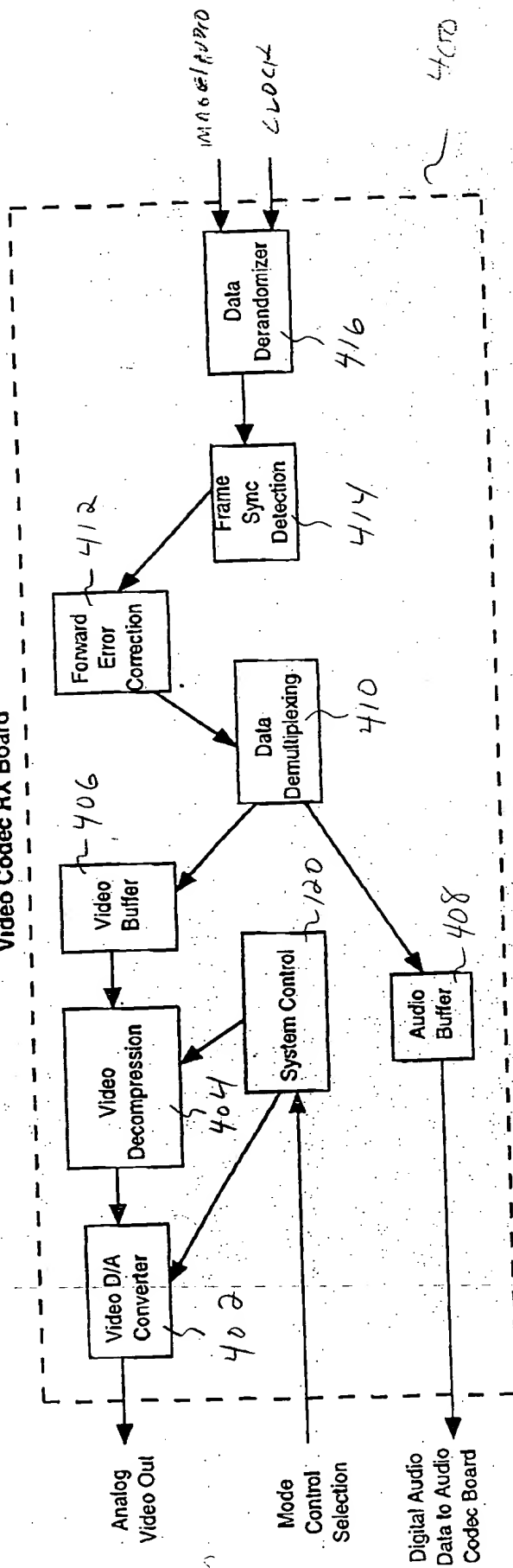


FIG. 3B

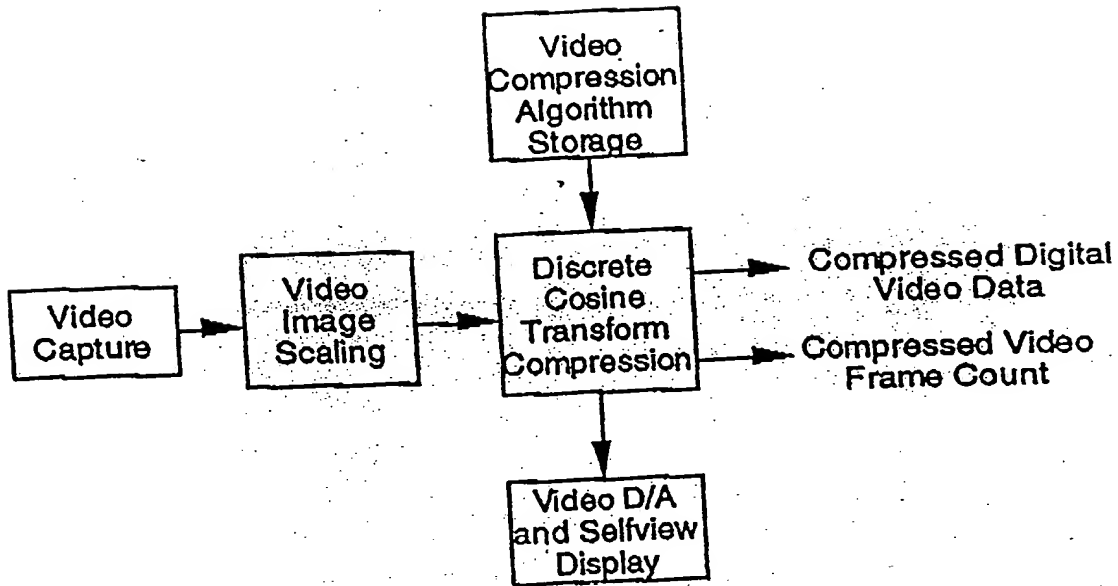


FIG. 4A

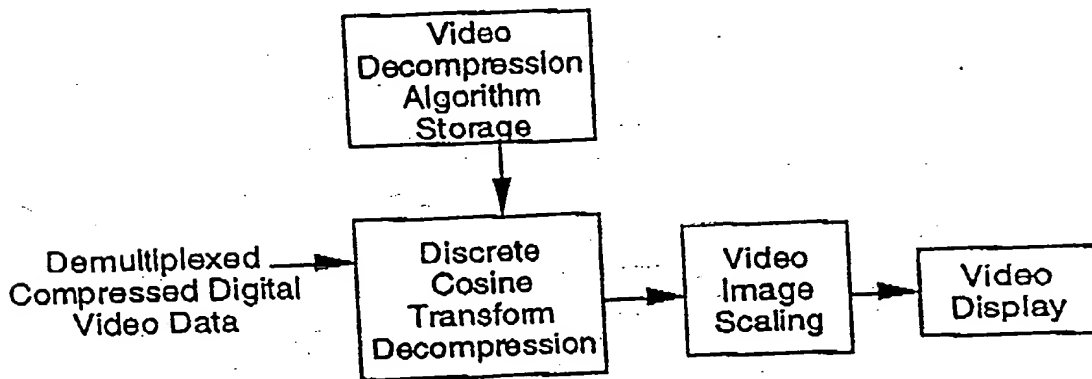


FIG 4B

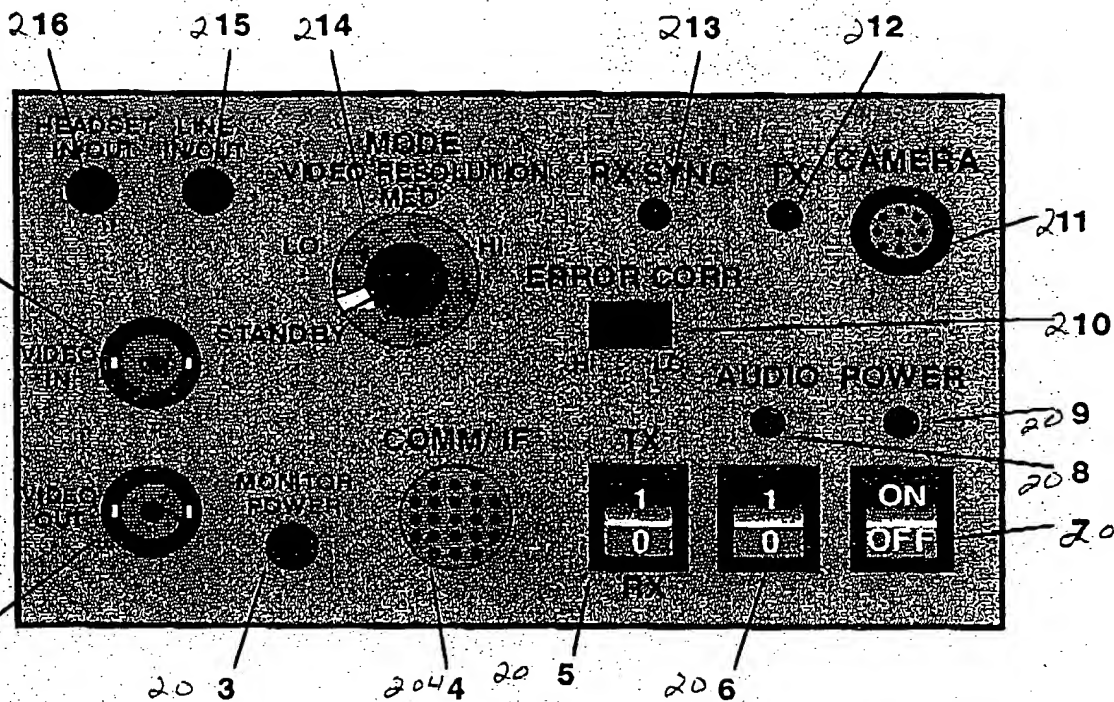


FIGURE 5a

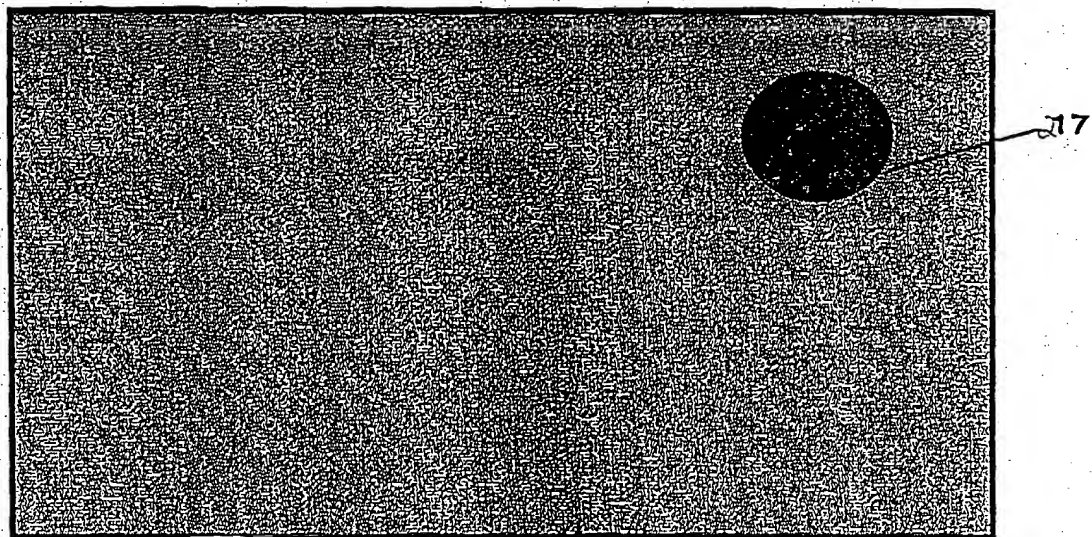


FIGURE 5b

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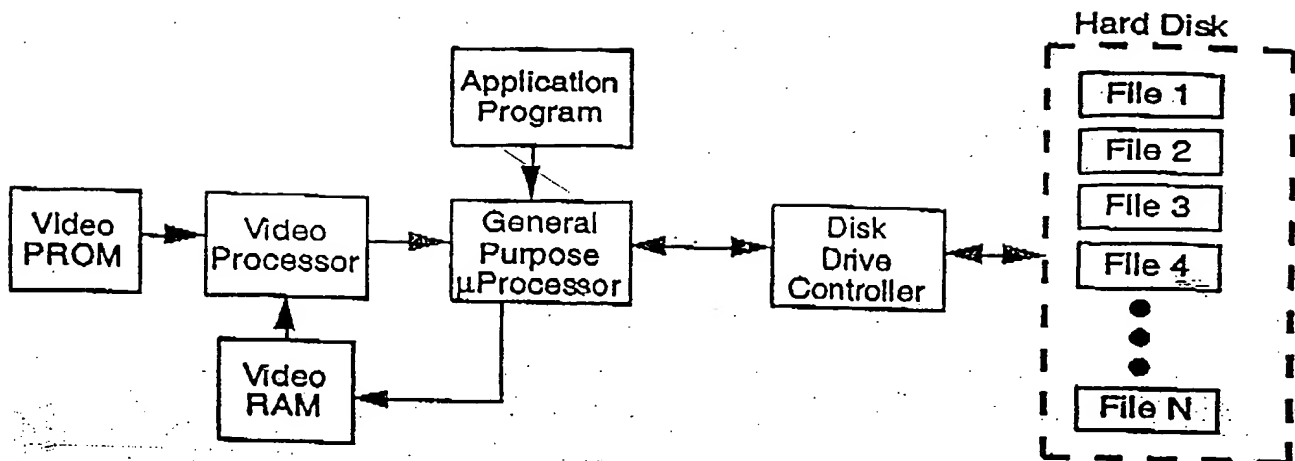


FIG. 6A

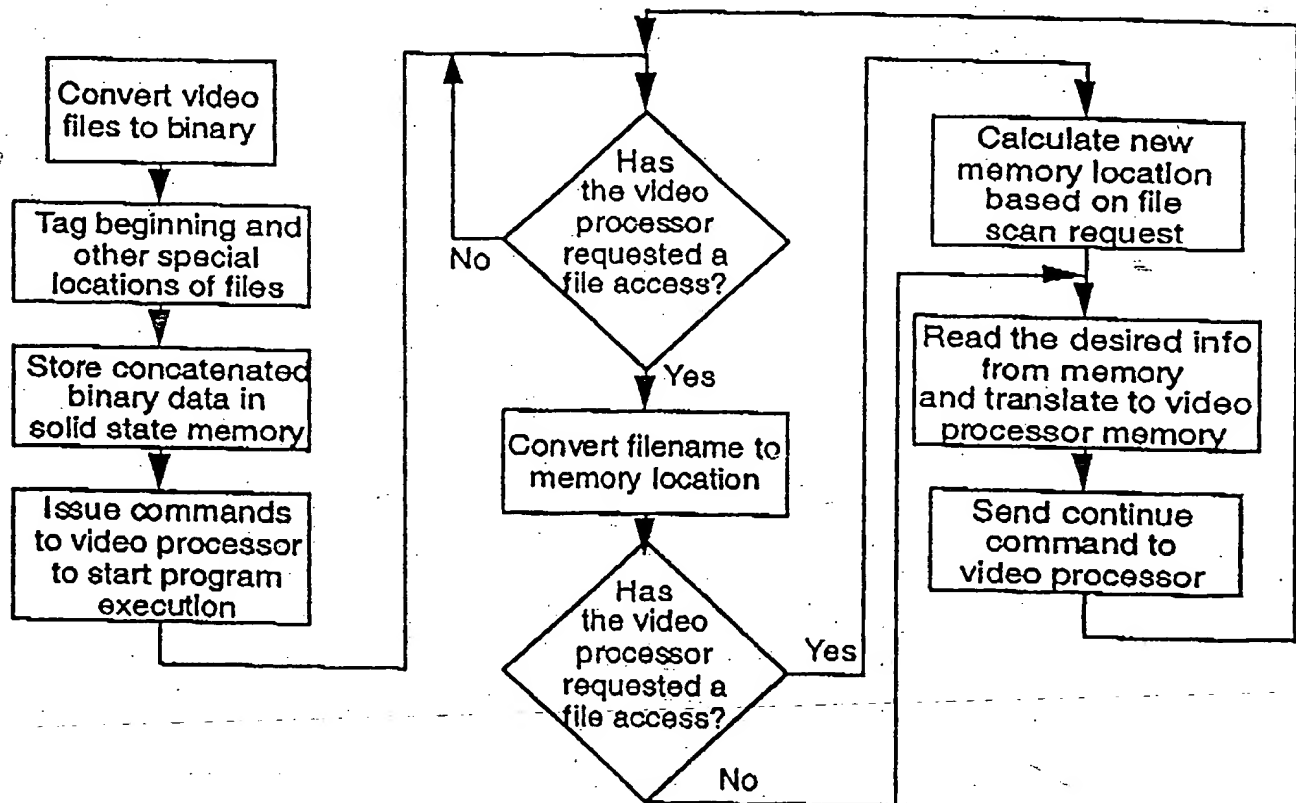


FIG. 6B

Clock Rate - 16 kHz or 32 kHz

Frame Size - 200 bytes (LO Error Correction Mode)

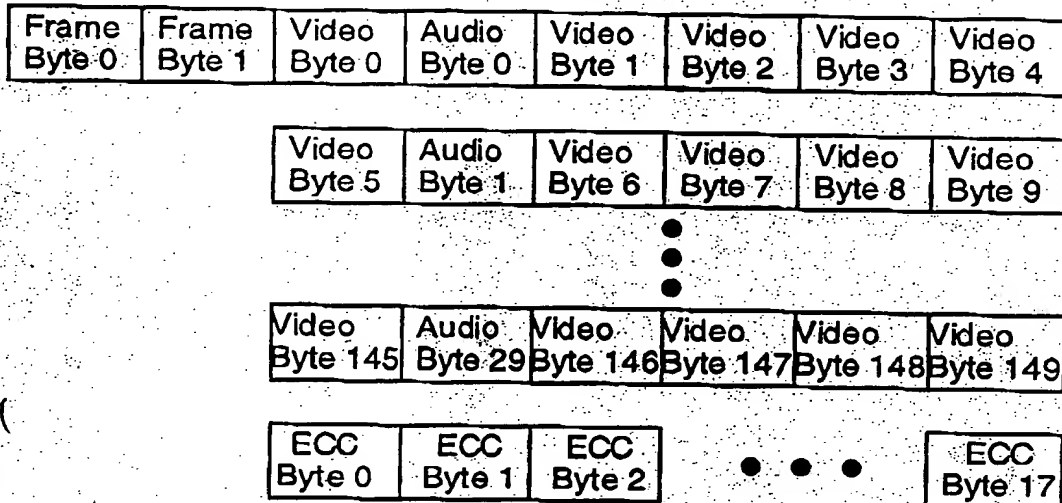


FIGURE 7a

Clock Rate - 64 kHz or 128 kHz

Frame Size - 200 bytes (LO Error Correction Mode)

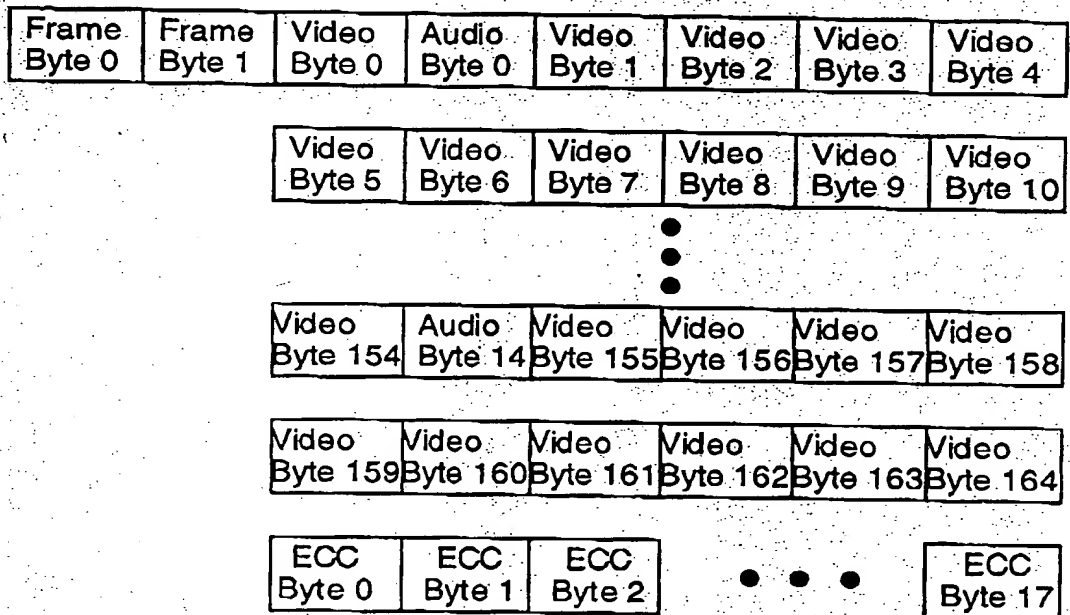


FIGURE 7b

**Clock Rate - 16 kHz or 32 kHz**  
**Frame Size      bytes (HI Error Correction Mode)**

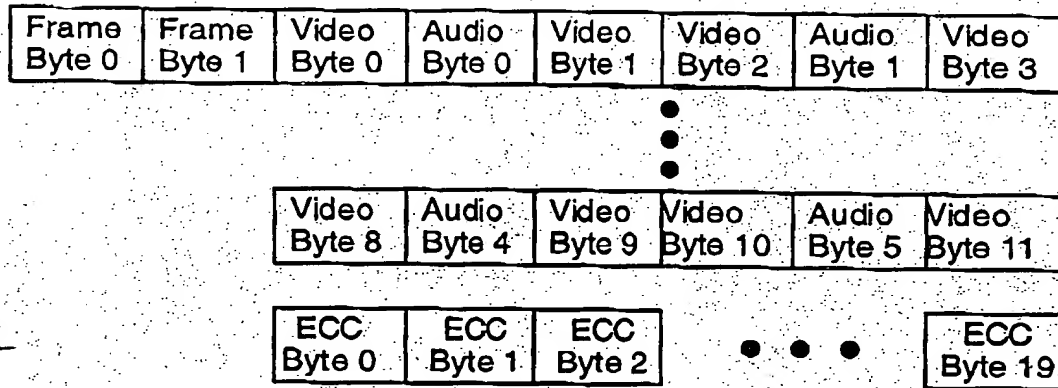


FIGURE 7c

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**Clock Rate - 64 kHz or 128 kHz**  
**Frame Size      bytes (HI Error Correction Mode)**

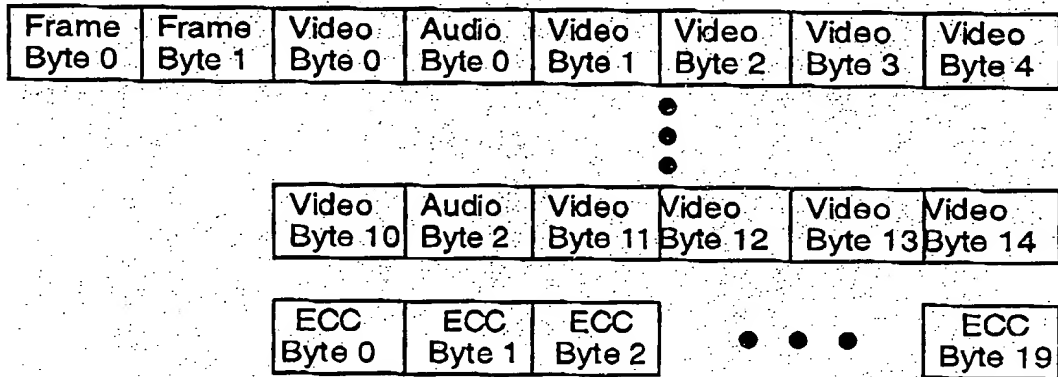
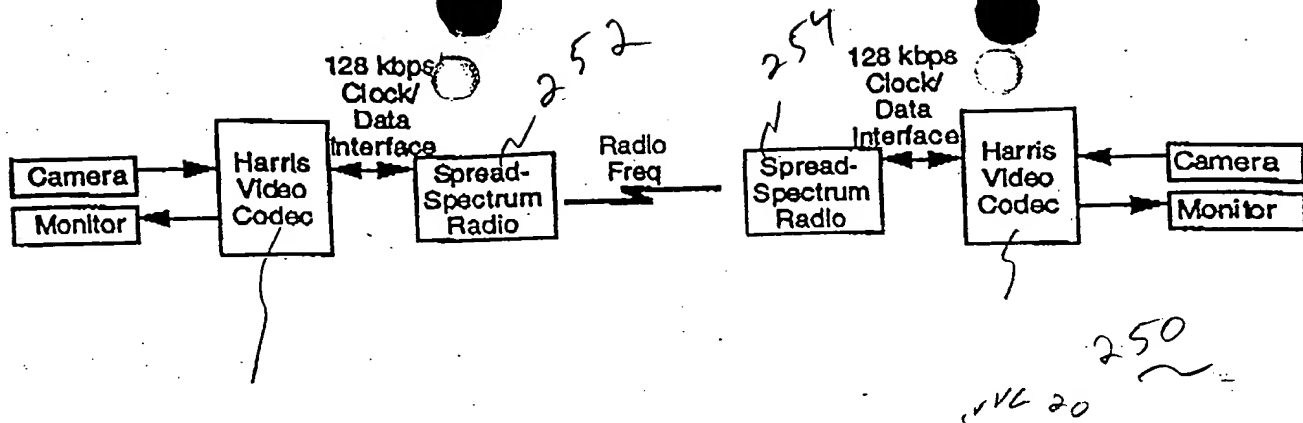


Figure 9.0-1 - Video/Audio Frame Structure

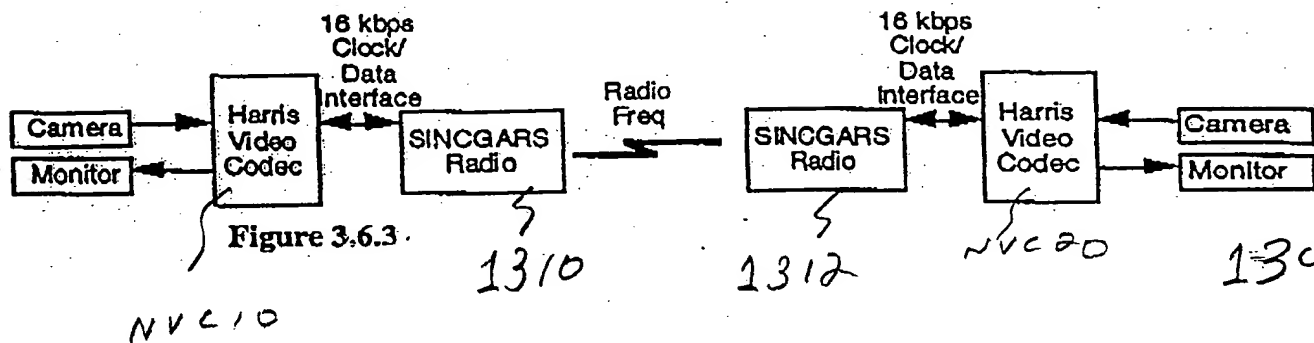
FIGURE 7d





NVC 10

FIG. 8



NVC 10

FIG. 9

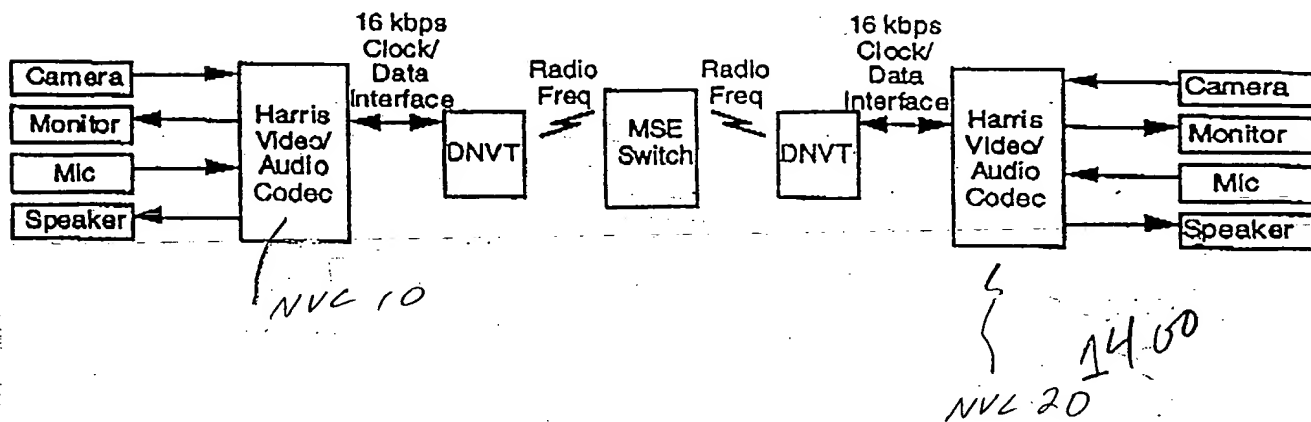


FIG. 10

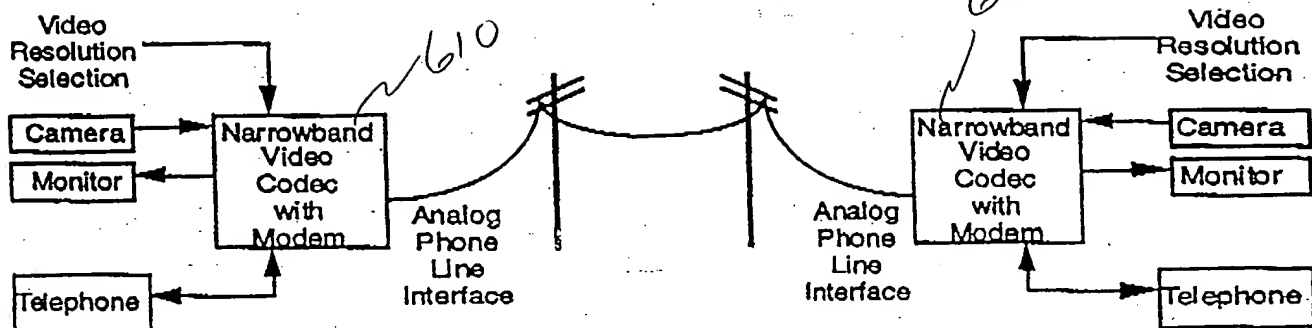


Figure 3.6.5

Fig 11

600

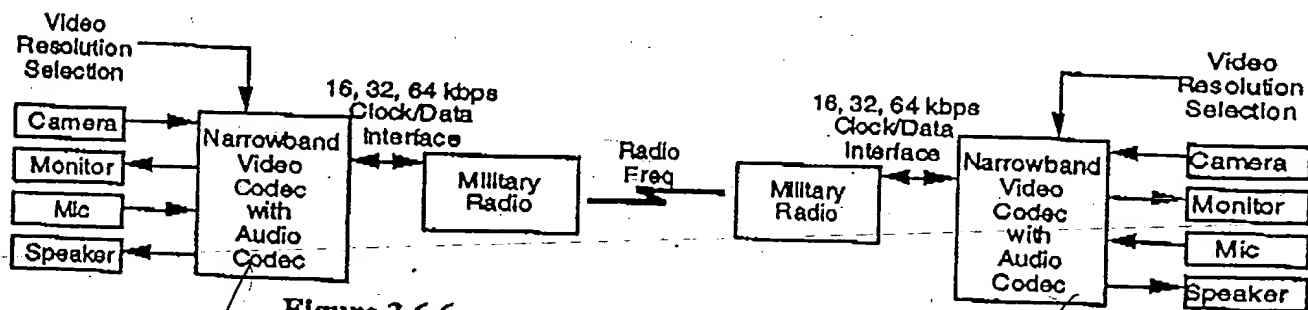


Figure 3.6.6

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Fig

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